Cool Power Technologies

Eighth-Brick Isolated DC/DC Converter

Features

- Input voltage range: 38 60Vin
- Output: 12V at 16.7A, 200W max.
- High Efficiency 94% Typical @ FL
- ROHS II Directive 2011/65/EU Compliant
- No minimum load required
- Low height 0.465" (11.8mm) max.
- Baseplate Optional 0.500" (12.7mm) tall
- Basic Insulation
- Fixed-frequency operation
- Industry standard 1/8th brick footprint
- Fully protection (OTP, OCP, OVP, UVLO auto-restart)
- Remote ON/OFF positive or negative enable logic options
- Remote sense
- Output voltage trim range: ±10% (industrystandard trim equations)
- Weight: 0.79 oz [22.4 g]
- · On-board input differential LC-filter
- Meets UL94, V-0 flammability rating
- Compliant to REACH (EC) No 1907/2006
- Compliant to UL/CSA60950-1, TUV per IEC/EN60950-1, 2nd edition
- Designed to meet Class B conducted emissions per FCC and EN55022 when used with external filter (see EMC Compliance section below.)



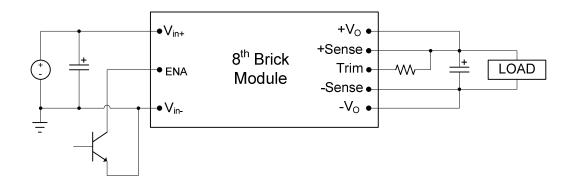
Description

The CPE17B50 "Cool Power Technologies" DC-DC converter is an open frame eighth-brick DC-DC converter that conforms to industry standard specifications. The converter operates over an input voltage range of 38 to 60 VDC, and provides a tightly regulated output voltage with an output current rating of 16.7 Amps. The output is fully isolated from the input and the converter meets Basic Insulation requirements. The standard feature set includes remote On/Off (positive or negative enable), input undervoltage lockout, output overvoltage protection, overcurrent and short circuit protections, output voltage trim, remote sense and overtemperature shutdown with hysteresis. The high efficiency of the CPE17B50 allows operation over a wide ambient temperature range with minimal derating (see Characteristic Curves section below.)

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APPLICATION DIAGRAM



ELECTRICAL SPECIFICATIONS

38-60Vin, 12V/16.7Aout

Conditions: T_A = 25 °C, Airflow = 300 LFM, Vin = 48 VDC, Cin = 100 μ F, unless otherwise specified.

Input Characteristics						
Parameter	Conditions	Min	Тур	Max	Unit	
Operating Input Voltage Range		38	48	60	VDC	
Input Under-Voltage Lock-out Turn-on Threshold Turn-off Threshold		34.2 32.4	35.0 33.2	35.9 34.1	VDC	
Input Voltage Transient	100ms			80	VDC	
Maximum Input Current	$V_{IN} = 38VDC; I_{out} = 16.7A$			5.7	Α	
Input Standby Current	Converter Disabled		2	5	mA	
Input No-Load Current	Converter Enabled		80	100	mA	
Short Circuit Input Current	RMS		30		mA	
Input Reflected Ripple Current	5Hz to 50MHz See Fig 11 for setup		15	25	mA _{PK-PK}	
Input Voltage Ripple Rejection	120Hz		50		dB	
Inrush Current	All	-	-	0.1	A²/s	
Output Characteristics						
Parameter	Conditions	Min	Тур	Max	Unit	
Output Voltage Set point	Sense pins connected to output pins	11.82	12	12.18	VDC	
Output Current		0		16.7	Α	
Output Current Limit Inception		17.5	20	25	Α	
Peak Short-Circuit Current	10mΩ Short			30	Α	
RMS Short-Circuit Current	10mΩ Short			3.0	A _{RMS}	
External Load Capacitance		0		4700	uF	
Output Ripple and Noise	20MHz Bandwidth 1 uF Ceramic + 10uF Tantalum See Fig 12 for setup		80	150	mV _{PK-PK}	
Output Regulation Line: Load: Overall Output Regulation:	Over line, load & temp.	11.64	±5 ±5	±10 ±10 12.36	mV mV V	

ELECTRICAL SPECIFICATIONS (continued)

38-60Vin, 12V/16.7Aout

Conditions: T_A = 25 °C, Airflow = 300 LFM, Vin = 48 VDC, Cin = 100 μ F, unless otherwise specified.

Absolute Maximum Ratings						
Parameter	Conditions	Min Typ		Max	Unit	
Input Voltage	Continuous Operation	0		60	VDC	
Operating Ambient Temperature	With Derating	-40		85	°C	
Operating Temperature	T _{ref} , see Thermal Derating section	-40		+123	°C	
Storage Temperature		-55		+125	°C	
Feature Characteristics						
Parameter	Conditions	Min	Min Typ		Unit	
Switching Frequency			350		kHz	
Output Voltage Trim Range ¹		-10		+10	%	
Remote Sense Compensation ¹				+10	%	
Output Over-voltage Protection	Non-latching	115	125	140	%	
Over-temperature Protection	Avg. PCB temp, non-latching		135		°C	
Peak Backdrive Output Current during startup into prebiased output			500		mA	
Backdrive Output Current in OFF state			0	5	mA	
Enable to Output Turn-ON Time	$V_{OUT} = 0.9*V_{OUT_NOM}$		20		ms	
Output Enable ON/OFF Negative Enable Converter ON Converter OFF	All voltages are WRT -Vin.	-0.5 2.4		0.8 20	VDC VDC	
Positive Enable Converter ON Converter OFF Enable Pin Current Source/Sink	Converter has internal pull-up of approx. 5V	2.4 -0.5	0.25	20 0.8 1	VDC VDC mA	
Output Voltage Overshoot @ Startup			0	2	%Vo	
Auto-Restart Period	(all protection features)		100		ms	



Technical Datasheet

CPE17B50

ELECTRICAL SPECIFICATIONS (continued)

38-60Vin, 12V/16.7Aout

Conditions: Ta = 25 °C, Airflow = 300 LFM, Vin = 48 VDC, Cin=100 μ F, unless otherwise specified.

Efficiency						
Parameter	Conditions	Min	Тур	Max	Unit	
Full Load	Vin = 48Vin	92.5	94		%	
50% Load	VIII – 40 VIII	93	94.5		%	
Dynamic Response						
Parameter	Conditions	Min	Тур	Max	Unit	
Load Change 50%-75% or 25% to 50% of lout Max, di/dt = 0.1 A/µs	Co = 1 μF ceramic + 10 μF tantalum		150	200	mV	
Settling Time to 1% of Vout	ι το μι ταπταιαπι		50		μs	
Load Change 25%-75% of lout Max, di/dt = 1 A/µs	Co = 1 µF ceramic		200	400	mV	
Settling Time to 1% of Vout	+ 1000 µF electrolytic		50		μs	
Isolation Specifications	Isolation Specifications					
Isolation Capacitance			1000		pF	
Isolation Resistance		10			MΩ	
Isolation Voltage – Input to Output				2250	V _{DC}	
Reliability						
Per Telcordia SR-332, Issue 2:	MTFB	2,281,055		Hours		
Method I, Case 3 (I_0 =80% of I_0 _max, T_A =40°C, airflow = 200 lfm, 90% confidence)	FITs (failures in 10 ⁹ hours)	438		/10 ⁹ Hours		

Notes:

1) Combination of remote sense + trim up not to exceed 10% of V_{onom} .

CHARACTERISTIC CURVES:

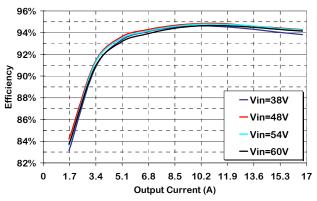


Figure 1. Efficiency vs Output Current, 300lfm airflow, 25 ℃ ambient.

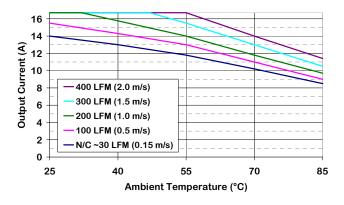


Figure 3. Output Current Derating vs Ambient Temperature & Airflow (air flowing from pin 3 to pin 1, Vin = 48 V, without baseplate)

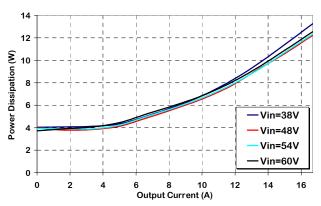


Figure 2. Power Dissipation vs. Load Current, 300lfm airflow, 25 ℃ ambient.

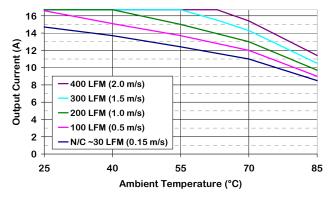


Figure 4. Output Current Derating vs Ambient Temperature & Airflow (air flowing from pin 3 to pin 1, Vin = 48 V, with baseplate)

CHARACTERISTIC WAVEFORMS:

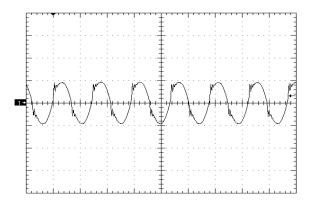


Figure 5. Output Voltage Ripple (50mV/div), time scale – 2uS/div. Vin=Vin_nom, full resistive.

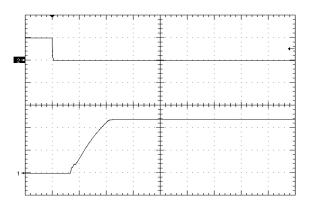


Figure 7. Startup Waveform via Enable Pin, time scale 10mS/div. Vin=Vin_nom, full resistive load (negative enable.) Ch1,Ch2=5V/div

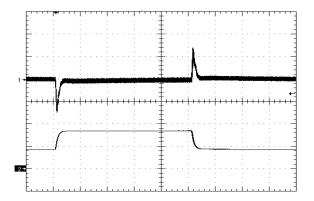


Figure 9. Load Transient Response (100mV/div), di/dt=0.1A/uS, 25% - 50% - 25% of full load, time scale: 200uS/div.

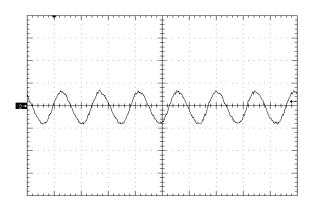


Figure 6. Input Reflected Ripple Current (10mA/div) time scale - 2uS/div. Vin=Vin_nom, full resistive.

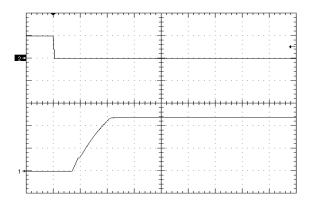


Figure 8. Startup Waveform via Enable Pin, time scale 10mS/div. Vin=Vin_nom, full resistive load + 2200uF (negative enable.) Ch1,Ch2=5V/div

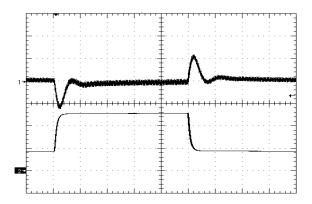
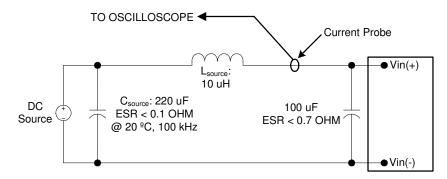


Figure 10. Load Transient Response (100mV/div), di/dt=0.5A/uS, 25% - 75% - 25% of full load, +1000uF Oscon, time scale: 200uS/div

Application Notes

Input Voltage Reflected Ripple Measurement

INPUT REFLECTED RIPPLE TEST SETUP:

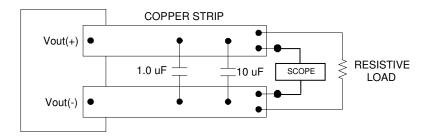


Note: Measure input reflected-ripple current with a simulated source inductance (Ltest) of 10 uH. Capacitor C_S offsets possible source impedance.

Figure 11. Input Reflected-ripple Current Test Setup.

Output Voltage Ripple Measurement

• OUTPUT RIPPLE TEST SETUP:



Note: Use a $1\mu F$ X7R ceramic capacitor and a $10\mu F$ tantalum capacitor. Scope measurement should be made using a BNC socket. Position the load 3 in. [76mm] from module.

Figure 12. Peak-to-Peak Output Noise Measurement Test Setup.

Output Voltage Trim

Output voltage adjustment is accomplished by connecting an external resistor between the Trim Pin and either the +Sense or –Sense pins.

• TRIM UP EQUATION:

$$R_{\text{trim_up}} = \left[\frac{5.1 \times \text{Vo_nom} \times (100 + \Delta\%)}{1.225 \times \Delta\%} - \frac{510}{\Delta\%} - 10.2 \right] \times \text{k}\Omega$$

Where R_{trim_up} is the resistance value in k-ohms and $\Delta\%$ is the percent change in the output voltage. E.g. to trim the output up 10%, $R_{trim_up} = \left[\frac{5.1 \times 12 \times (100 + 10)}{1.225 \times 10} - \frac{510}{10} - 10.2\right] \times k\Omega$ or $R_{trim_up} = 488$ kOhm.

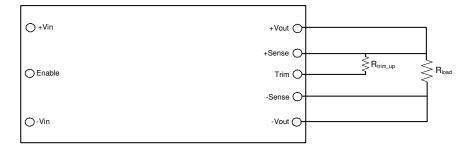


Figure 13. Trim UP circuit configuration

• TRIM-DOWN EQUATION:

$$R_{\text{trim_down}} = \left(\frac{510}{\Delta\%} - 10.2\right) \times k\Omega$$

Where $R_{trim down}$ is the resistance value in k ohms and $\Delta\%$ is the percent change in the output voltage.

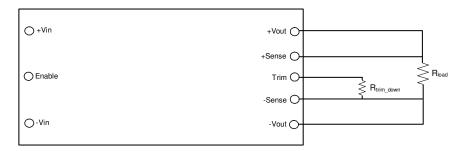
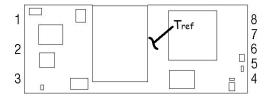
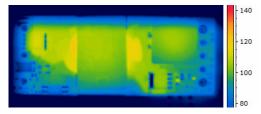


Figure 14. Trim DOWN circuit configuration

Thermal Derating

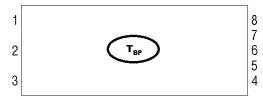
- It is preferable that the DC-DC module have an unobstructed flow of air across it for best thermal performance. Components taller than ~ 2mm in front of the module can deflect airflow and possibly create hotspots.
- Significant cooling is achieved through conductive flow from the modules I/O pins to the host PCB. Sufficiently large traces connecting the dc-dc converter to the source and load will help ensure thermal derating performance will meet or exceed the derating curves published in this datasheet.
- If the module is expected to be operated near the load limits defined in the derating curves, insystem verification of module derating performance should be performed to ensure long-term system reliability. Peak temperatures are to be measured using infrared thermography or by gluing a fine gauge (AWG #40) thermocouple at the T_{ref} location(s) shown below. Temperatures at the specified location(s) are not to exceed 123°C in order to maintain converter reliability. For baseplate models, T_{BP} should not exceed 115°C.





Open Frame Measurement Points

Thermal Image of module @ 48Vin, 200LFM & 40C



Baseplate Measurement Point

Input Undervoltage Lockout

• The converter is disabled until the input voltage has exceeded the UVLO turn-on threshold. Once the input voltage exceeds this level (see Input Under-Voltage Lock-out in Electrical Specifications table) the module will commence soft-start. Hysteresis of 2-3 volts minimizes the likelihood of pulling the input voltage below the turn-off threshold during startup which could create an undesirable on/off cycling condition. The converter will continue to operate until the input voltage subsequently falls below the UVLO turn-off threshold.

Enable Pin Function

- The module has a remote enable function that allows it to be turned on or off remotely. The Enable pin is referenced to the negative input pin (-Vin) of the converter. Modules can be ordered with either negative or positive enable.
- The negative enable option the module will not turn on unless the enable pin is connected to Vin. The positive enable option allows the converter to turn on as soon as voltage sufficient to exceed the UVLO of the converter has been applied to the input terminals. In this case the module is turned off by connecting the Enable pin to –Vin. On/off thresholds are located in the Electrical Specifications table.

Output Overvoltage Protection

The module has an independent feedback loop that will disable the output of the converter if a
voltage greater than about 125% of the nominal set point is detected. When this threshold is
reached, the converter will shut down and remain off for the amount of time specified by the
Auto-Restart Period. The converter will attempt a restart once this period of time has elapsed.

Output Overtemperature Protection

• To provide protection under certain fault conditions, the unit is equipped with a thermal shutdown circuit. The unit will shutdown if the average PCB temperature exceeds approx. 135°C, but the thermal shutdown is <u>not</u> intended as a guarantee that the unit will survive temperatures beyond its rating. The module will automatically restart once it has cooled below the shutdown temperature minus hysteresis (typically 20 deg C.)

SMT Version Layout Considerations (if applicable)

- Copper traces with sufficient cross-section must be provided for all output & input pins. SMT pads tied to internal power/ground planes must have multiple vias around each SMT pad to couple expected current loads from module pins into internal traces/planes. One 0.024" (0.6mm) diameter via for each 4A of expected source or load current must be provided as close to the termination as possible, preferably in the direction of current flow from SMT pad to load. Vias must be at least 0.024" (0.6 mm) away from the SMT pad to prevent solder from flowing into the vias.
- SMT pads on the host card are to be 0.110" (2.79mm) diameter. Solder paste screen opening should be 0.105" diameter and the screen should be 0.006" (0.15 mm) thick (other thicknesses are possible; 0.006" provides a good compromise between solder volume and coplanarity compensation.)

Paralleling Converters

Modules may be paralleled but it is recommended that the total power draw not exceed the
output power rating of a single module. External sharing controllers are recommended for
reliability and to ensure equal distribution of the load to the converters.



EMC Compliance

To meet Class B compliance for EN55022 (CISPR 22) or FCC part 15 sub part j, the following input filter is required:

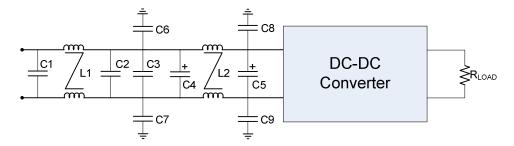


Figure 15. EMI Filter

L1, L2 =	0.59 mH Common Mode Inductor (Pulse P0353)
C1, C2, C3 =	2.2uF ceramic
C4 =	Not used
C5 =	100uF electrolytic
C6, C7 =	8.2nF (@2kV if output is ref. to gnd.)
C8, C9 =	8.2nF (@2kV if output is ref. to gnd.)

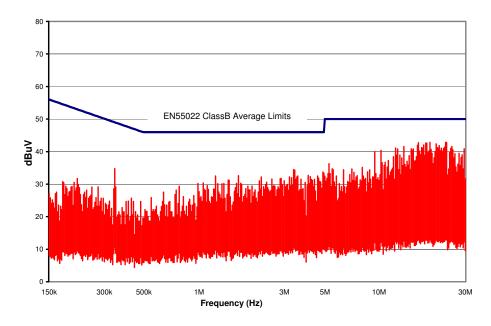
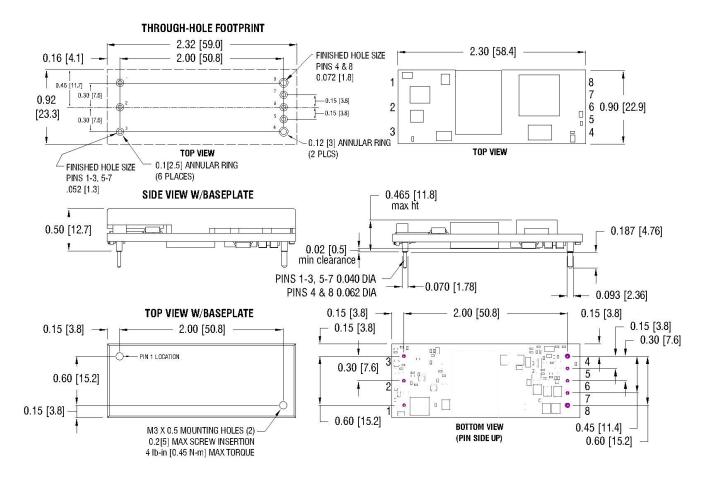


Figure 16. CPE17B50 Conducted Emissions using above specified input filter, Vin = 48V, Full Resistive Load

MODULE PIN ASSIGNMENT:

PIN#	DESIGNATION	NOTES
1	V _{IN} (+)	1) All dimensions in inches [mm]
2	On/Off	Tolerances: .xx ± 0.02 [.x ± .5] .xxx ± 0.010 [.xx ± .25]
3	V _{IN} (-)	2) Input, on/off control and sense/trim pins are Ø 0.040" [1.02]
4	V _{OUT} (-)	with Ø 0.070" [1.77] standoff shoulders.
5	Sense (-)	3) Output pins are Ø 1.57 mm (0.062") with Ø 0.093" [2.36] shoulders (note, shoulder sits .008" above mounting surface)
6	Trim	4) All pins are gold plated with nickel under plating.
7	Sense (+)	5) Weight: 22.4 g (0.79 oz.)
8	V _{OUT} (+)	6) Workmanship: Meet or exceeds IPC-A-610 Class II

MECHANICAL OUTLINE:



ORDERING INFORMATION:							
Product Identifier	Output Current	Output Voltage	Input Voltage	Enable logic option	Additional features		
CPE	17	В	50	N or P	В		
"Cool Power Eighth"	16.7A	12	38 – 60V	N = Negative P = Positive	B = Baseplate Option		

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